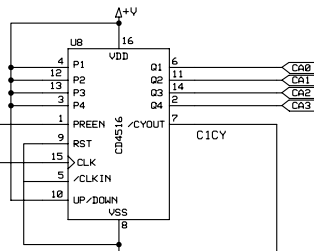


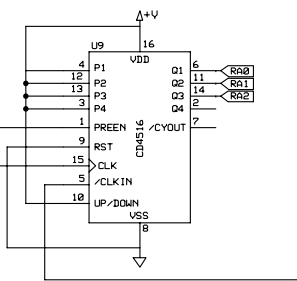
0000 - 1111
Sequentially selects columns

Counter 1 Column



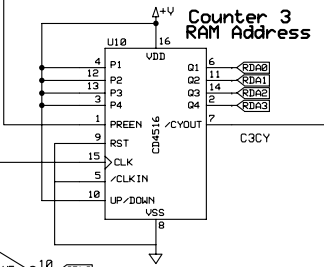
000 - 111
Sequentially selects 8 rows
One row selection for every
16 clock cycles
One RAM Write Pulse
For every 8 clock cycles

Counter 2 Row



0000 - 1111
Addresses Ram Data
Increments Once Per 8 clock cycles

Counter 3 RAM Address



Control Counter

